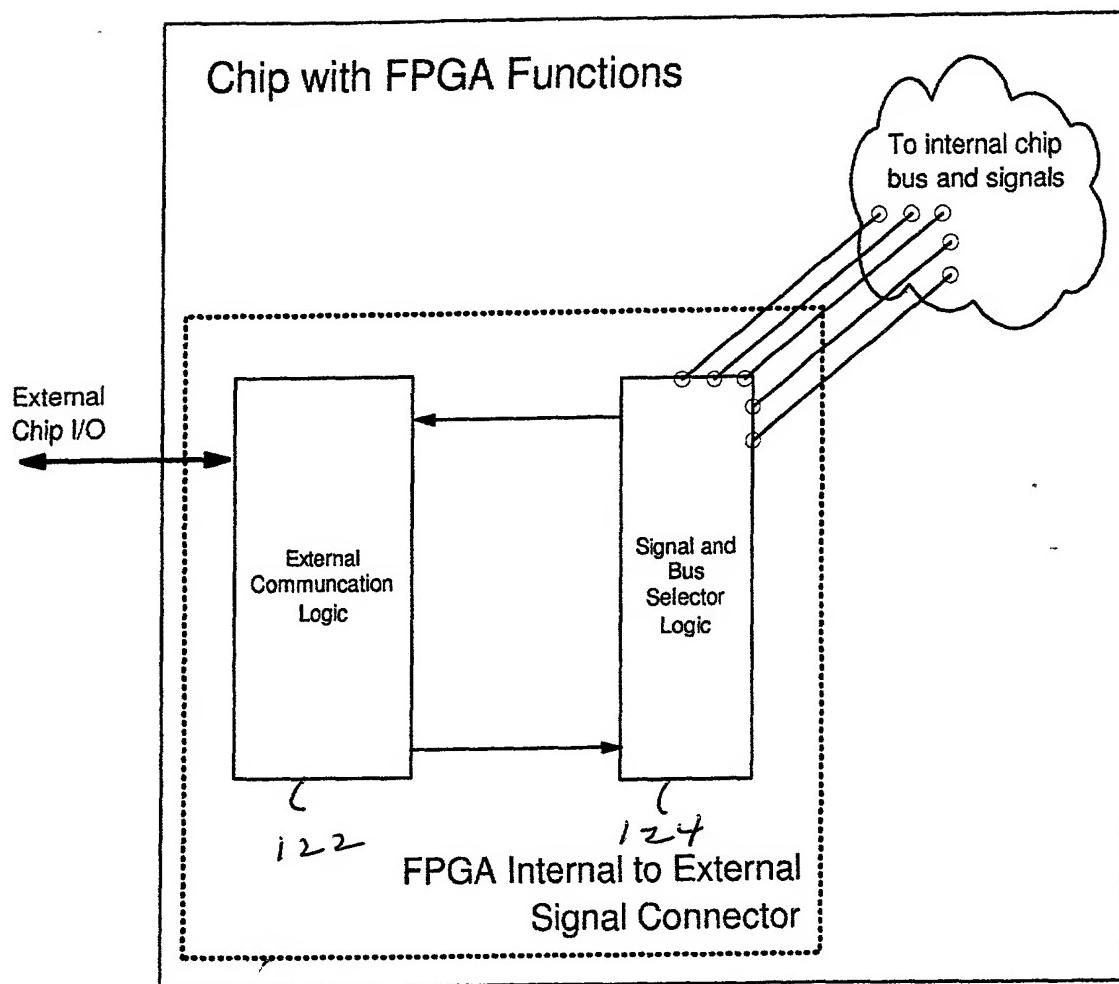


Fig. 1



120

Fig. 2

Monitoring internal bus reveals Function A is not sending correct values to Function B.

Drive correct values into Function B from FPGA and monitor output of Function B to determine if it is operating correctly

External Chip I/Os

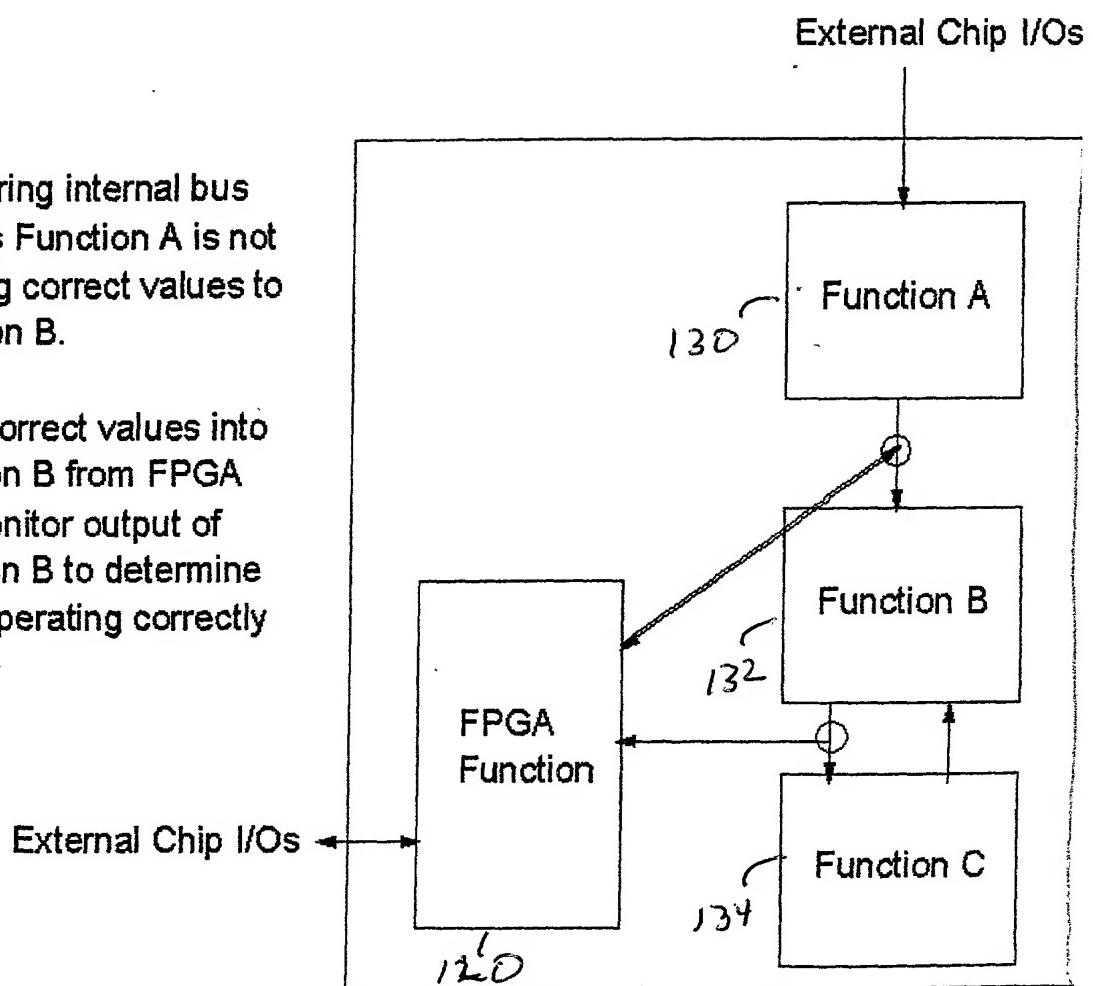
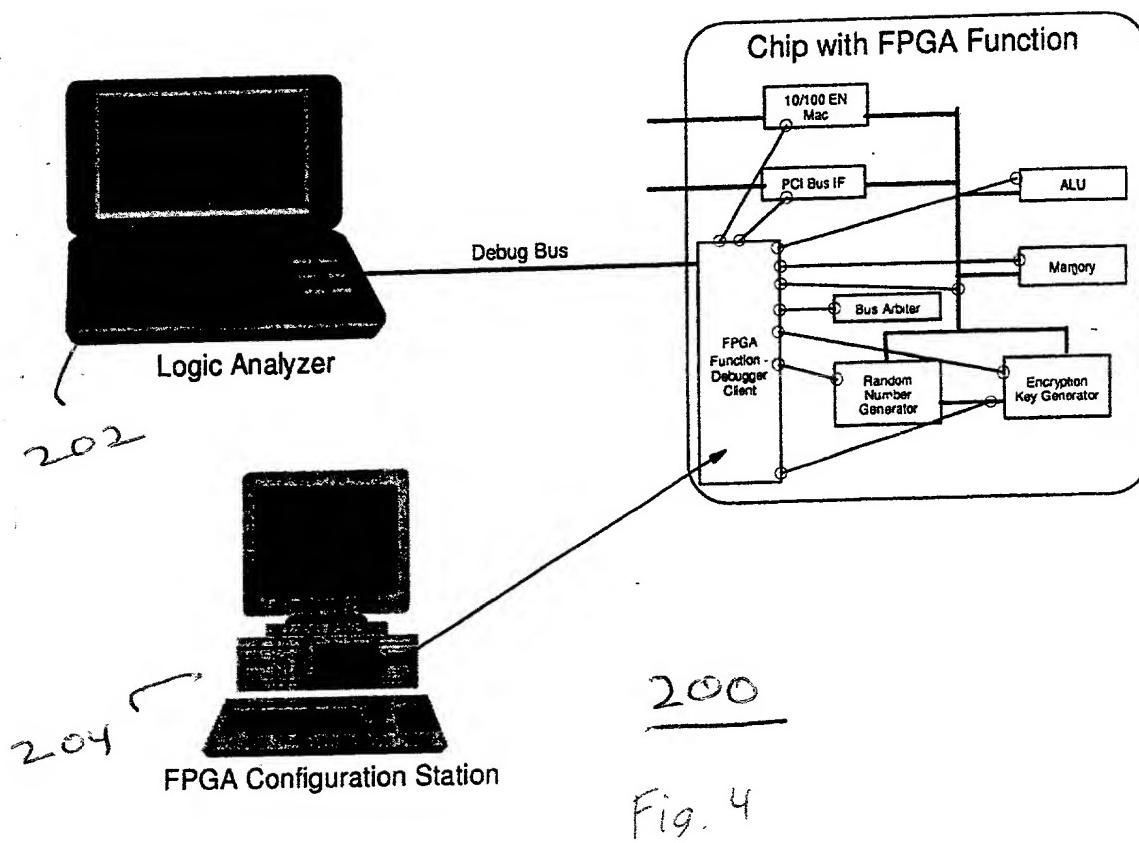
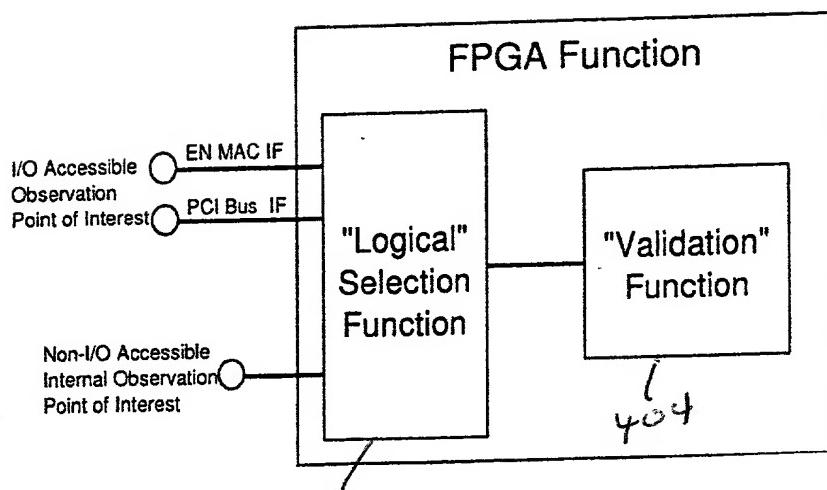


Fig. 3

- Memory and Memory Bus Debug
- MAC Bus Debug
- PCI, Internal Bus and Bus Arbiter Debug
- Processor Debug
- Clock and Clock Distribution Debug





402 Fig. 5

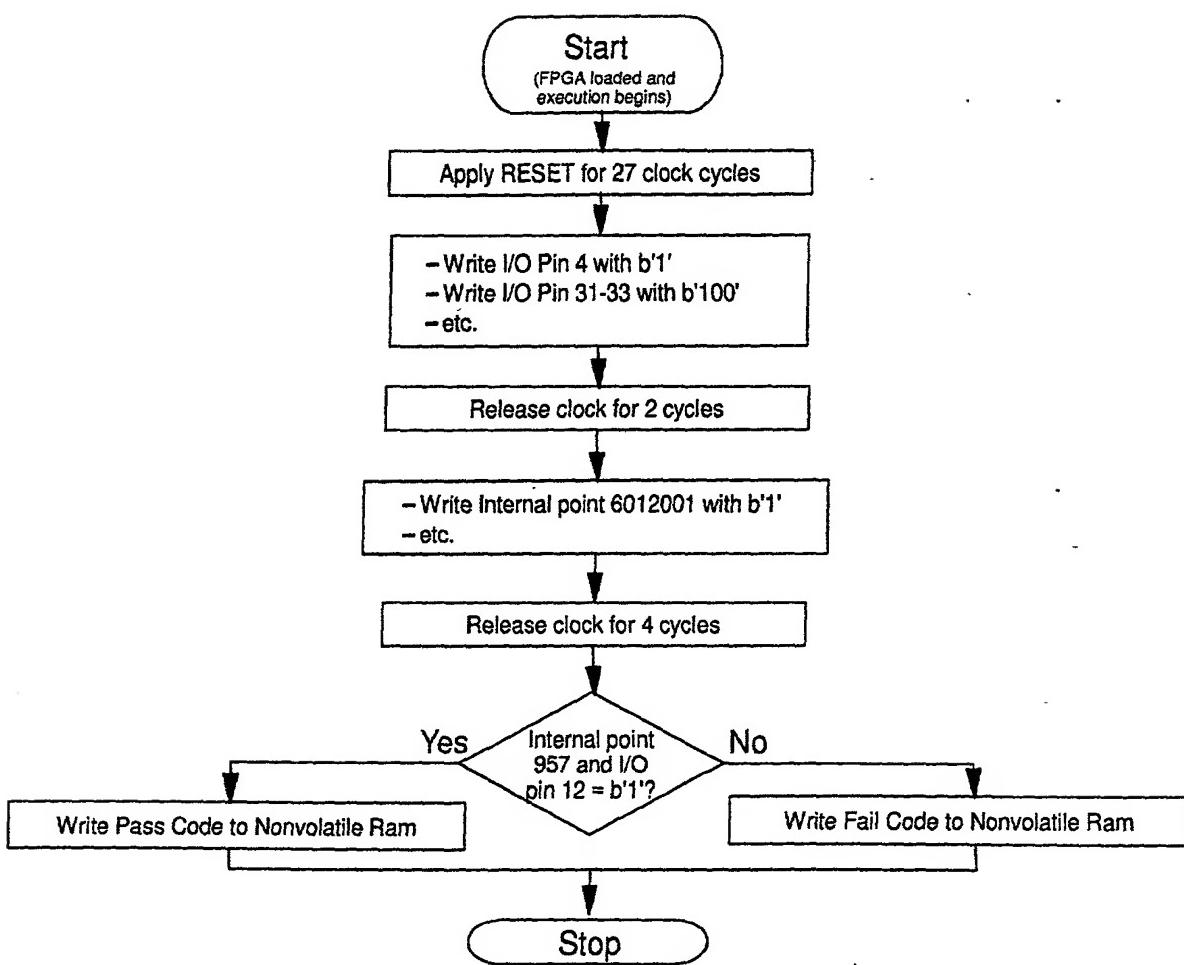


Fig. 6

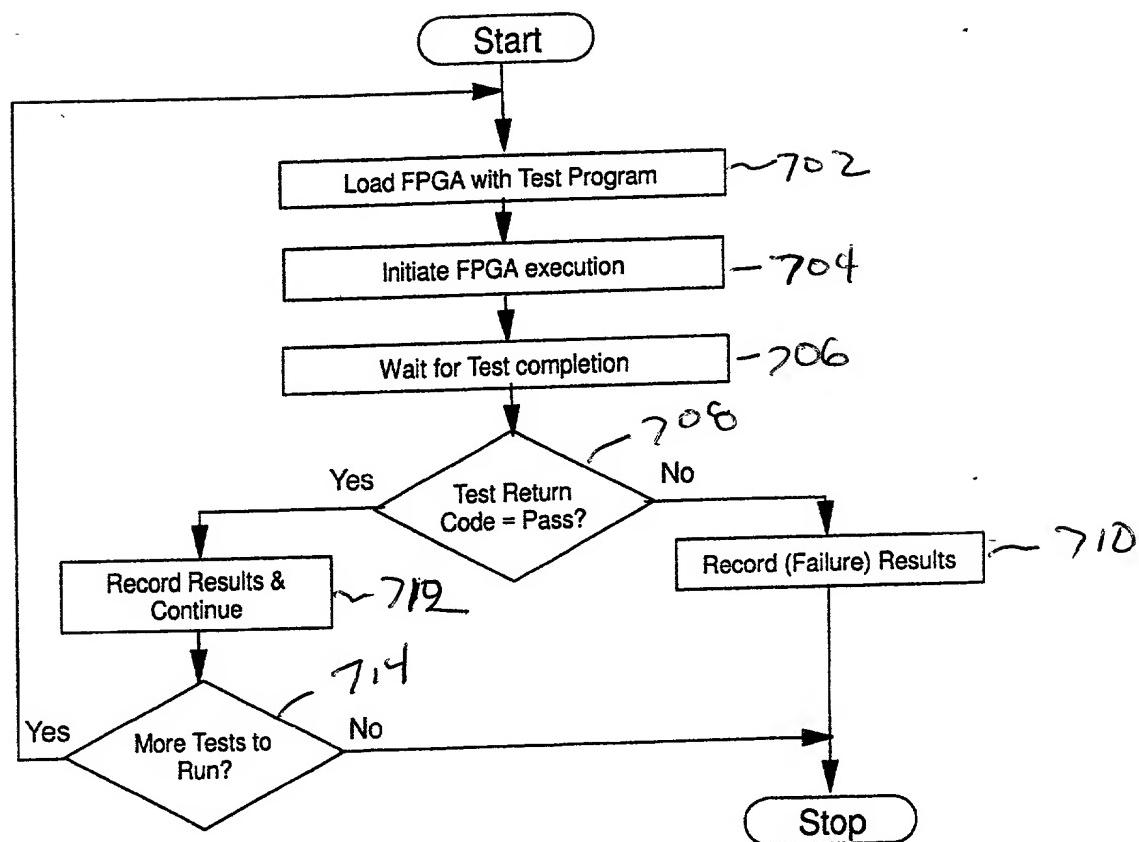


Fig. 7

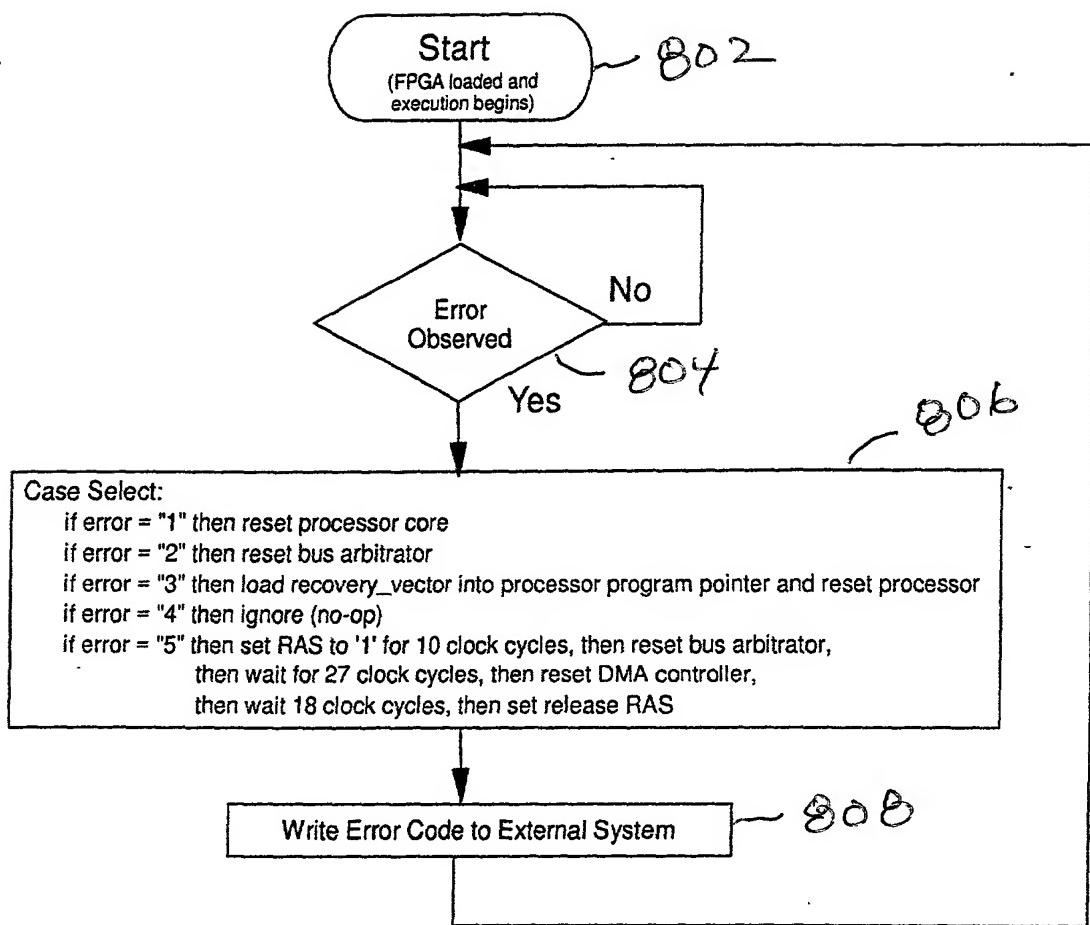


Fig. 8

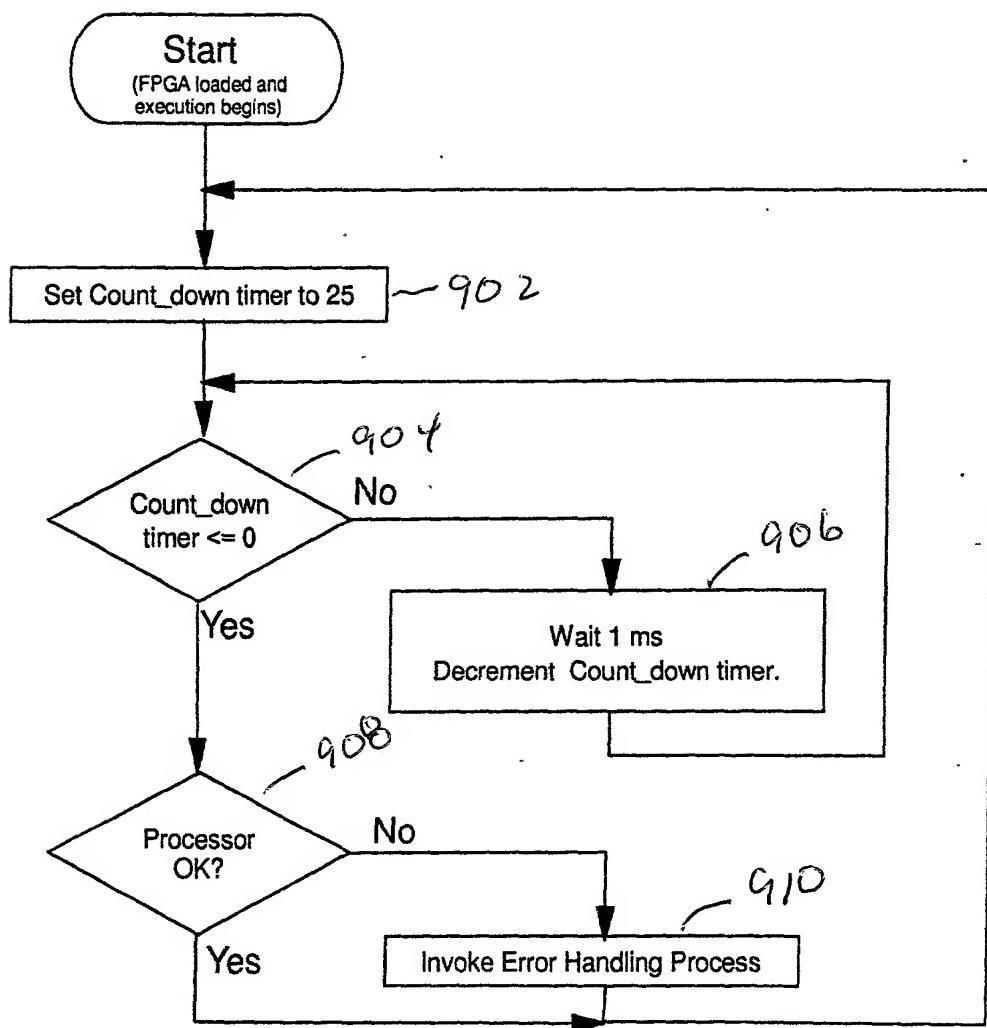


Fig 9